- 8 are electrically parallel to one another, each of said laterally designed bipolar transistors
- 9 includes a control connection connected to one of the voltage buses;
- a single track resistor (RB) co-integrated into a semiconductor body, wherein said
- single track resistor precedes every control connection (B) of said laterally designed bipolar
- 12 transistors (T1-T3).

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2.(previously amended) The electrostatic discharge protective structure of claim 1, wherein said semiconductor body has embedded therein at least one emitter zone and at least one collector zone of the first conduction type and at least one base zone of the second, opposite conduction type, wherein a well-shaped région is inserted into said semiconductor body between said zones of the first conduction type and said base zone or said base zones, so as to extend the effective mean free path of the charge carriers to said base zone.

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- 1 3. The electrostatic discharge protective structure of claim 2, wherein said well-shaped
- 2 region is connected to one of said zones of the first conduction type.

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- 4. The electrostatic discharge protective structure of claim 3, wherein said well-shaped
- 2 region has the same conduction type as said zone to which it is connected, but has a lower
- 3 dopant concentration.

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5. The electrostatic discharge protective structure of claim 4, wherein said well-shaped region extends deeper into said semiconductor body than said zone which it adjoins or to which it is connected.

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6.(previously amended) The electrostatic discharge protective structure of claim 5, wherein said base zones laterally enclose said emitter zones and said collector zones.

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7. The electrostatic discharge protective structure of claim 6, wherein said semiconductor body has charge carriers of the first conduction type and that at least one further well of the second conduction type is embedded in said semiconductor body, and that said emitter zones, collector zones, and base zones and said well-shaped regions are embedded in said well.

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1 8. The electrostatic discharge protective structure of claim 7, wherein said emitter zones
2 and collector zones are designed as strips and are disposed alternatingly next to one another
3 and parallel to one another.

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9. The electrostatic discharge protective structure of claim 8, wherein said electrostatic discharge protective structure is configured and arranged in an essentially square layout.

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- 1 10. The electrostatic discharge protective structure of claim 9, wherein in said emitter
- 2 zones are through-contacted by said emitter electrodes, and said collector zones are through-
- 3 contacted by said collector electrodes.

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1 11. The electrostatic discharge protective structure of claim 8, wherein said emitter

- 2 electrodes and said collector electrodes are connected via conductor tracks to oppositely
- 3 situated voltage buses and form finger-like connections which are staggered with one another.
- 1 12. The electrostatic discharge protective structure of claim 8, wherein said bipolar
- 2 transistors (T1-T3) are designed as field oxide transistors.